

REPLACEMENT

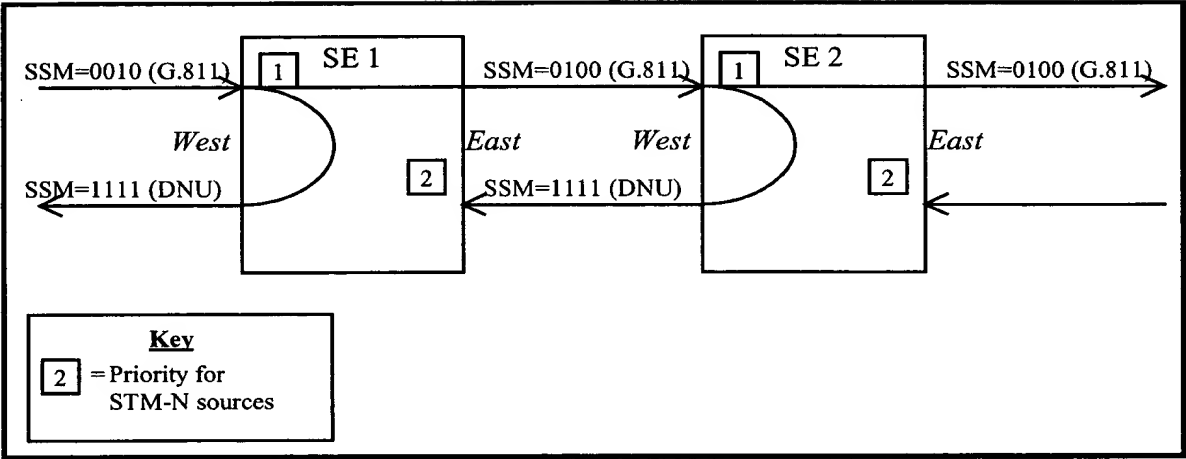


Figure 1 Timing Loop Prevention PRIOR ART

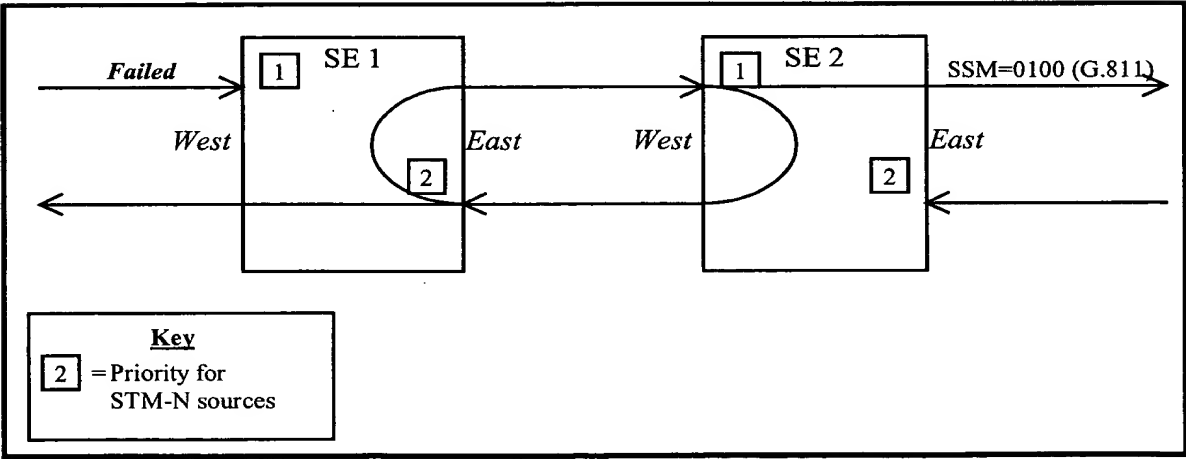


Figure 2 Synchronisation Timing Loop PRIOR ART

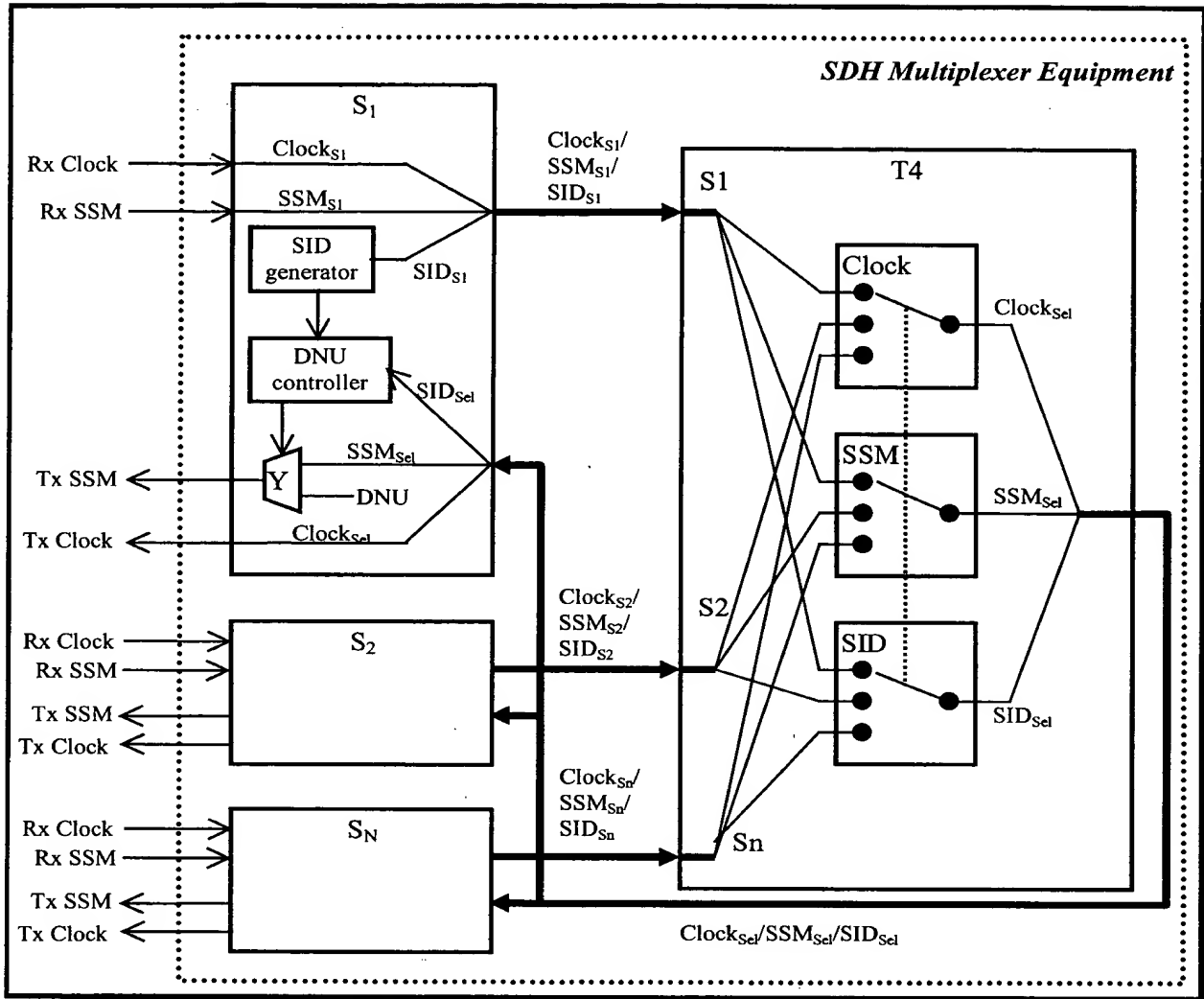
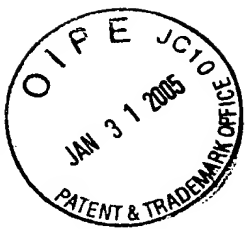


Figure 3 General use of SID in Timing Loop Prevention PRIOR ART



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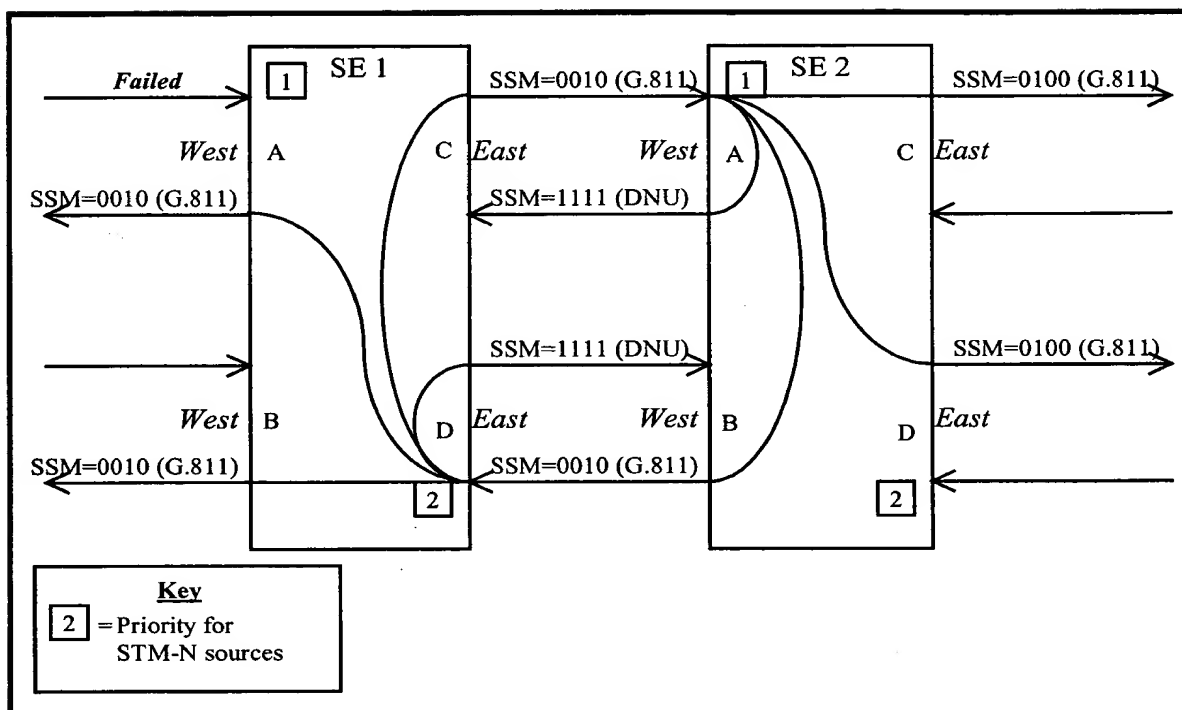


Figure 4 Multiple Port Timing Loop PRIOR ART

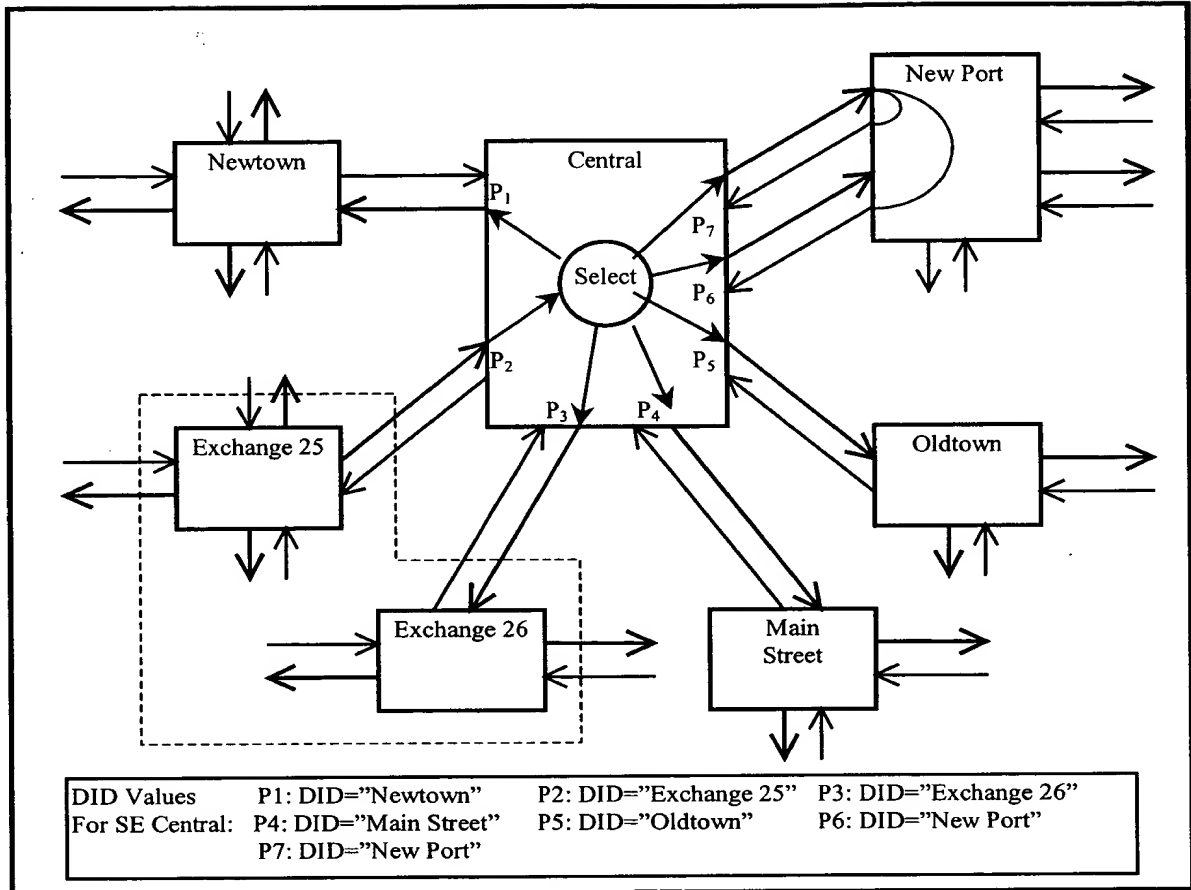


Figure 5 Destination IDs